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BC-2873

B. C. A. (Third Semester)

EXAMINATION, 2024-25

COMPUTER ARCHITECTURE AND ASSEMBLY LANGUAGE

Time : Two Hours

Maximum Marks : 75

Note : Attempt questions from all Sections as directed.

Section—A

(Very Short Answer Type Questions)

Note : Attempt any *five* questions. Each question carries 7 marks. 5×7=35

1. Explain program control unit of processor.
2. Explain any *one* mode of DMA.
3. What is operation code ?

P. T. O.

4. What is timing and central unit ?
5. What is accumulator ?
6. Explain *two* modes of data transfer.
7. What is parallel processing ?
8. Explain RISC.
9. Explain the term Cache Memory.
10. What is Assembly Language ?.

Section—B

(Short Answer Type Questions)

Note : Attempt any *two* questions. Each question carries 10 marks. 2×10=20

1. Explain the input/output Interface of peripheral devices.
2. Explain the following terms :
   (a) Brust transfer
   (b) Cyclic stealing
3. Explain the block diagram of 8085.
4. Explain Asynchronous data transfer.

Section—C

(Long Answer Type Questions)

Note : Attempt any *one* question. Each question carries 20 marks. 1×20=20

1. Explain the Booth's algorithm.
2. Solve any *two* number additions with algorithm.
3. Explain the use of macros in I/O instruction.
4. Explain register transfer instructions.

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